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XA-9593
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Takashi KOBAYASHI et al.

Appln. No.: 10/031,117

Group Art Unit: 2822

Filed: January 16, 2002

Examiner: M. Trinh

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND PROCESS
FOR PRODUCING THE SAME

Allowed: April 8, 2004

Confirmation No.: 3823

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AMENDMENT IN RESPONSE TO NOTICE OF
DRAWING INCONSISTENCY WITH SPECIFICATION

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Notice mailed June 10, 2004, please
amend the above-identified patent application as indicated
below.

Amendments to the Specification begin on page 2 of
this paper.

Remarks begin on page 3 of this paper.